

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. <b>67161-140</b>		SERIAL NO. <b>Divisional of Appl. No.</b> <b>10/077,833</b>			
(PTO-1449)						APPLICANT <b>Naoya WATANABE, et al.</b>					
						FILING DATE <b>April 13, 2004</b>		GROUP			
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EXAMINER'S INITIALS		CITE NO.	Document Number Number-Kind Code <sub>2</sub> (if known)		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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SN			JP 10-050958		02/20/1998						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)											
EXAMINER'S INITIALS		CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.								
SN			"Figure 9 SDRAM Functional Block Diagram", IEEE Micro, November/December 1997, pp. 38-39.								
SN			"A Configurable DRAM Macro Design for 2112 Derivative Organizations to be Synthesized Using a Memory Generator", YABE et al., 1998 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, pp. 72-73.								
EXAMINER						DATE CONSIDERED					
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